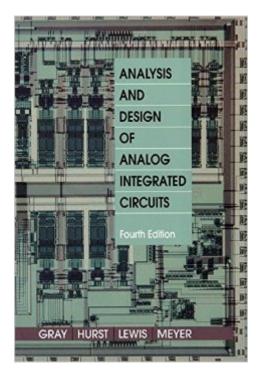
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Analysis And Design Of Analog Integrated Circuits (4th Edition)





Synopsis

The fourth edition features coverage of cutting edge topics--more advanced CMOS device electronics to include short-channel effects, weak inversion and impact ionization. In this resourceful book find: * Coverage of state-of-the-art IC processes shows how modern integrated circuits are fabricated, including recent issues like heterojunction bipolar transistors, copper interconnect and low permittivity dielectric materials * Comprehensive and unified treatment of bipolar and CMOS circuits helps readers design real-world amplifiers in silicon.

Book Information

Hardcover: 875 pages Publisher: Wiley; 4 edition (February 15, 2001) Language: English ISBN-10: 0471321680 ISBN-13: 978-0471321682 Product Dimensions: 7.3 x 1.6 x 10 inches Shipping Weight: 3.4 pounds Average Customer Review: 4.6 out of 5 stars Â See all reviews (37 customer reviews) Best Sellers Rank: #190,899 in Books (See Top 100 in Books) #28 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Integrated #54 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design #89 in Books > Computers & Technology > Computer Science > Systems Analysis & Design

Customer Reviews

Required reading, I would think, for anyone in the business, or studying to get into the business, of design & development of analog IC's. If you haven't already been introduced to this book, better make it a part of your library.You'll find good stuff on: Large & small signal BJ & MOS transistor models, An informative chap. on IC technology & construction, Good chap. on current mirrors & active loads, Several chaps. on amps. including a detailed analysis of 741, Good info. on frequency response & analysis of IC opamps., Chaps. on feedback & stability of IC opamps, A lot of attention to MOS technology throughout, Loads of transistor equations & useful approximations.Also there's other material which should interest anyone in analog circuit design, including discrete design. There's even an interesting chap. on circuit noise, which for me had been a subject with about as much sex appeal as Thermodynamics.Be forwarned, this isn't for the novice circuit designer. It's a rather rough trudge to make your way through the book. But as far as I'm capable of critically

reviewing, this is a very well written book with very few errors (4th edition). There're a few quirks here & there. For example they introduce their own symbols for MOS transistors (as if we don't have enough), over play SPICE as "an integral part of many examples", & make stability analysis witht Bode plots (Bode not credited) more complicated than need be. However, these are trivial when compared to the overall work. I would therefore give the authors a resounding 5 stars. However, I can't do so for the version of the book which I received. I bought the book for self-study purposes. I was very disappointed to find 20 to 40 problems @ the end of each chap.

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